

**PATENT** 

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Customer Number: 20277

OISHI, HIDEO

Confirmation Number: 2253

Application No.: 10/602,878

Group Art Unit: 2812

Filed: June 25, 2003

Examiner: STEVENSON, Andre C.

For: SEMICONDUCTOR WAFER TEST SYSTEM

## RESPONSE TO RESTRICTION REQUIREMENT

Mail Stop Restriction Requirement Honorable Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Noting the Office Action of October 6, 2005 wherein restriction has been required, Applicant hereby elects Group I (claims 1-12) for prosecution in the above-identified application.

To the extent necessary, a petition for an extension of time under 37 C.F.R. 1.136 is hereby made. Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

McDERMONT WILL & EMERY LLP

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Facsimile: 202.756.8087 Date: November 2, 2005 Please recognize our Customer No. 20277 as our correspondence address.